



JFW

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Takahiro ONIZUKA et al.

Group Art Unit: 2831

Application No.: 10/694,756

Examiner: D. R. Patel

Filed: October 29, 2003

Docket No.: 117480

For: CASING UNIT FOR CIRCUIT ASSEMBLY AND METHOD FOR
PRODUCING THE CIRCUIT ASSEMBLY

**CONFIRMATION OF TELEPHONE INTERVIEW
AND EXAMINER'S AMENDMENT**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Further to the June 29, 2004 telephone interview between Applicants' representative and Examiner Patel, agreement was reached on the following items:

1. Examiner Patel objected to claims 8-11 due to informalities. In particular, Examiner Patel stated that antecedent basis was inconsistent for features in the method claims that depend from apparatus claims. Applicants' representative agreed to replace articles "a" and "an" with --said-- for elements first recited in the apparatus claims by Examiner's amendment.
2. Upon correction of the above-identified informalities, the application is in condition for allowance.

No new matter is added by any of these amendments. Prompt allowance is earnestly solicited. Should the Examiner believe that anything further is desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact Applicants' undersigned representative at the telephone number listed below.

Respectfully submitted,

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JAO:GWT/gwt

Date: July 6, 2004

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